

ABSTRACT OF THE DISCLOSURE

It is an object of the present invention to provide a display apparatus which can be miniaturized and is operated stably even
5 at high resolution.

The display apparatus according to the present invention includes a pixel array unit, a signal line driving circuit, and a scanning line driving circuit, each of which is formed by using a polysilicon TFT on a glass substrate, a control circuit, and
10 a graphic controller IC. Since the graphic controller IC rearranges digital pixel data DATA in the inside, it is unnecessary to provide a gate array. Since the cycle of a clock signal CLK is twice as much as that of the digital pixel data DATA, the clock signal CLK having a frequency at which the polysilicon TFT normally
15 operates can be supplied to the signal line driving circuit. Further, since the edge of the clock signal CLK is deviated from the changing position of the digital pixel data DATA and they are outputted, the signal line driving circuit can effectively capture the digital pixel data DATA.

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